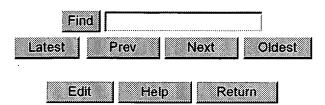
S22817	U	USPT	display\$ with light with emit\$ and semiconductor	2004_02_11
344017	<u>U</u>	OSF I	with substrate with n\$ near channel and buffer with circuit and electrolumi\$ and TFT	
<u>S22816</u>	<u>U</u>	USPT	display\$ with light with emit\$ and semiconductor with substrate with n\$ near channel and buffer with circuit and electrolumi\$	
<u>S22815</u>	<u>U</u>	USPT	display\$ with light with emit\$ and semiconductor with substrate with n\$ near channel and buffer with circuit	2004-03-11 23:13:44
<u>S22814</u>	<u>U</u>	USPT	display\$ with light with emit\$ and semiconductor with substrate with n\$ near channel	2004-03-11 23:12:51
<u>S22813</u>	<u>U</u>	USPT	6017983.pn. and semiconductor	2004-03-11 23:06:31
<u>S22812</u>	<u>U</u>	USPT	6017983.pn. and pixel\$	2004-03-11 22:36:31
<u>\$22811</u>	<u>U</u>	USPT	6017983.pn. and pixels	2004-03-11 22:36:20
<u>S22810</u>	<u>U</u>	USPT	6017983.pn. and 191	2004-03-11 22:35:25
<u>S22809</u>	<u>U</u>	USPT	5663589.pn. and display\$	2004-03-11 22:28:21
<u>S22808</u>	<u>U</u>	USPT	light with emit\$ with display\$ and pixels with substrate and driv\$ with substrate and 6107983.pn. and light\$ with emit\$	2004-03-11 22:23:54
S22807	<u>U</u>	USPT	light with emit\$ with display\$ and pixels with substrate and driv\$ with substrate and decod\$ with plural\$ with logic	2004-03-11 22:08:20
S22806	<u>U</u>	USPT	light with emit\$ with display\$ and pixels with substrate and driv\$ with substrate and decod\$ with plural\$ with "NAND"	2004-03-11 22:07:05
<u>S22805</u>	<u>U</u>	USPT	light with emit\$ with display\$ and pixels with substrate and driv\$ with substrate	2004-03-11 22:05:58
S22804	<u>U</u>	USPT	light with emit\$ with display\$ and pixels with substrate and driv\$ with substrate and driv\$ with decod\$ with plural\$ with "NAND"	2004-03-11 22:05:17
<u>S22803</u>	<u>U</u>	USPT	light near emit\$ with display\$ and pixels with substrate and decoder with plural\$ with logic near circuits	2004-03-11 21:59:58
<u>S22802</u>	<u>U</u>	USPT	light near emit\$ with display\$ and pixels with substrate and decoder with plural\$ logic near circuits	2004-03-11 21:59:43
S22801	<u>U</u>	USPT	6498596.pn. and logic	2004-03-11 21:56:07
<u>S22800</u>	<u>U</u>	USPT	6498596.pn. and flip\$	2004-03-11 21:55:58
<u>S22799</u>	<u>U</u>	USPT	5663589.pn. and flip\$	2004-03-11 21:55:44
<u>S22798</u>	<u>U</u>	USPT	5663589.pn. and flip\$	2004-03-11

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L				
				21:54:55
S22797	<u>U</u>	USPT	5663589.pn. and 2010b	2004-03-11
				21:45:28
S22796	<u>U</u>	USPT	5663589.pn. and logic	2004-03-11
				21:43:37
<u>S22795</u>	<u>U</u>	USPT	5663589.pn. and NAND	2004-03-11
				21:43:26
<u>S22794</u>	<u>U</u>	USPT	5181029.pn. and logic	2004-03-11
				21:41:49
<u>S22793</u>	$\underline{\mathbf{U}}$	USPT	6498596.pn. and NAND	2004-03-11
				21:38:27
<u>S22792</u>	$\underline{\mathbf{U}}$	USPT	6677221.pn. and NAND	2004-03-11
				21:37:57
<u>S22791</u>	<u>U</u>	USPT	6677221.pn. and logic	2004-03-11
				21:32:05
<u>S22790</u>	<u>U</u>	USPT	6677221.pn. and buffer	2004-03-11
				21:31:24
<u>S22789</u>	<u>U</u>	USPT	6677221.pn. and logic	2004-03-11
				21:30:06
<u>S22788</u>	<u>U</u>	USPT	light\$ with emit\$ with pixel with driv\$ near circuit	
			with substrate and NAND	21:17:55
<u>S22787</u>	<u>U</u>	USPT	light\$ with emit\$ with pixel with driv\$ near circuit	
			with substrate and plural\$ with NAND	21:17:17
<u>S22786</u>	<u>U</u>	USPT	light\$ with emit\$ with pixel with driv\$ near circuit	
			with substrate	21:15:31
S22785	<u>U</u>	USPT	light\$ with emit\$ with pixel with driv\$ near circuit	
			44	21:13:23
S22784	<u>U</u>	USPT	6677221.pn. and shift\$	2004-03-11
				21:10:34
<u>S22783</u>	<u>U</u>	USPT	6677221.pn. and enhanc\$	2004-03-11
	• •		((77001	21:10:20
<u>S22782</u>	<u>U</u>	USPT	6677221.pn. and flip\$	2004-03-11
				21:07:53
<u>S22781</u>	<u>U</u>	USPT	6677221.pn. and flip\$	2004-03-11
				21:07:36



<u>S22863</u>	<u>U</u>	USPT	6372558.pn. and substrate	2004-03-12
<u>S22862</u>	<u>U</u>	USPT	6372558.pn. and plastic near substrate	02:15:41 2004-03-12 02:15:10
<u>S22861</u>	<u>U</u>	USPT	6372558.pn. substrate	2004-03-12 02:14:25
<u>S22860</u>	<u>U</u>	USPT	6169391.pn. and shift	2004-03-12 02:06:49
<u>S22859</u>	<u>U</u>	USPT	6169391.pn. and screen	2004-03-12 01:58:12
<u>S22858</u>	<u>U</u>	USPT	6169391.pn.	2004-03-12 01:56:16
<u>S22857</u>	<u>U</u>	USPT	6169391.pn. and substrate	2004-03-12 01:53:38
S22856	<u>U</u>	USPT	6169391.pn. substrate	2004-03-12 01:53:02
<u>S22855</u>	<u>U</u>	USPT	6169391.pn. and display\$	2004-03-12 01:50:13
<u>S22854</u>	<u>U</u>	USPT	6169391.pn. and 50 with 51	2004-03-12 01:40:02
<u>S22853</u>	<u>U</u>	USPT	6169391.pn.	2004-03-12 01:35:30
<u>S22852</u>	<u>U</u>	USPT	gate with connect\$ with drain with transistor and display\$ and enhancement with depletion with transistor and shift with register with flip near flop	2004-03-12 00:44:09
<u>S22851</u>	<u>U</u>	USPT	gate with connect\$ with drain with transistor and display\$ and enhancement with depletion with transistor and flip near flop	2004-03-12 00:43:38
<u>S22850</u>	<u>U</u>	USPT	gate with connect\$ with drain with transistor and display\$ and enhancement with depletion with transistor	2004-03-12 00:43:18
<u>\$22849</u>	<u>U</u>	USPT	gate with connect\$ with drain with transistor and display\$ and enhancement with depletion	2004-03-12 00:43:01
<u>S22848</u>	<u>U</u>	USPT	gate with connect\$ with drain with transistor and display\$	2004-03-12 00:42:25
<u>S22847</u>	<u>U</u>	USPT	gate with connect\$ with drain transistor and display\$ and enhancement with depletion with transistor	2004-03-12 00:41:36
<u>S22846</u>	<u>U</u>	USPT	gate with connect\$ with drain transistor and display\$	2004-03-12 00:40:36
<u>S22845</u>	<u>U</u>	USPT	gate near first near transistor with second near drain near tansistor	2004-03-12 00:39:38
<u>S22844</u>	<u>U</u>	USPT	gate near first near TFT with second near drain near TFT and display\$	2004-03-12 00:39:06
<u>S22843</u>	<u>U</u>	USPT	gate near first near TFT with second near drain near TFT	2004-03-12 00:38:42
<u>S22842</u>	<u>U</u>	USPT	buffer with first near TFT with drain second near TFT with gate and display\$ and transistor\$ near	2004-03-12 00:34:02

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Ì			types	
C22041	U	USPT	types buffer with first near TFT with drain second near	2004-03-12
<u>S22841</u>	<u>U</u>	USFI	TFT with gate and display\$ and transistor\$ with	00:31:40
			types	00.31.40
S22840	U	USPT	buffer with first near TFT with drain second near	2004-03-12
322040	<u>U</u>	OSII	TFT with gate and display\$ and all with transistor\$	
			with types	00.51.25
S22839	U	USPT	buffer with first near TFT with drain second near	2004-03-12
022032	<u> </u>		TFT with gate and display\$ and transistor\$ with	00:31:12
			types	00.51.12
S22838	U	USPT	buffer with first near TFT with drain second near	2004-03-12
	_		TFT with gate and display\$ and transistor\$ with	00:27:31
			enhanc\$ and transistor with deple\$	
S22837	<u>U</u>	USPT	buffer with first near TFT with drain second near	2004-03-12
			TFT with gate and display\$ and transistor\$ with	00:26:56
			enhanc\$	
S22836	<u>U</u>	USPT	buffer with first near TFT with drain second near	2004-03-12
			TFT with gate and display\$ and shift with flip\$	00:25:29
			near flop	
<u>S22835</u>	$\underline{\mathbf{U}}$	USPT	buffer with first near TFT with drain second near	2004-03-12
			TFT with gate and display\$ and shift near register	00:24:49
<u>.</u>			with flip\$	
<u>S22834</u>	<u>U</u>	USPT	buffer with first near TFT with drain second near	2004-03-12
			TFT with gate and display\$	00:24:01
<u>S22833</u>	<u>U</u>	USPT	buffer with first near TFT with drain second near	2004-03-12
			TFT with gate	00:23:50
<u>S22832</u>	<u>U</u>	USPT	6107983.pn. and gate with drain	2004-03-12
				00:19:05
<u>S22831</u>	<u>U</u>	USPT	6372558.pn. and gate with drain with connect\$	2004-03-12
				00:16:05

